International Symposium on Electronic System Design 2010
December 20-22, 2010, The Crown Hotel, Bhubaneswar, India.
Call for Participation (http://ised.seedsnet.org)

3 Keynotes from Leaders of Academia and Industry

- Jacob A. Abraham, Cockrell Family Regents Chair Professor at the University of Texas at Austin: "Moore's Law and Beyond: Electronic Design Challenges"
- Praveen Vishakantaiah, Intel India President: "Trends and Advances in Computing and Communication"
- Karthikeyan Natarajan, Global Head of Integrated Engineering Solutions at Mahindra Satyam, India: "Rapid Emergence and Convergence of Electronic Technologies as Multiplying Factor for Innovation Capability"

8 Plenary Talks from Experienced Researchers

- Pin-Constrained Designs of Digital Microfluidic Biochips for High-Throughput Bioassays
- Introducing Energy Efficiency into Graphics Processors
- Design and Validation of Robust Systems
- Skip the Analysis: Self-Optimizing Networks-on-Chip
- A Practitioner's Approach to Software System Design
- History and Trends in Analog Circuit Challenges at the System Level
- Using Test and Diagnosis to Enable Tomorrow's Robust Systems
- Power Management in Multi-source Environments

For Future Researchers and Academicians : 6 Talks in Workshop for Engineering Scholars (WES)

- A Successful Faculty Member: Essential Components
- Interconnect and Device Scaling Trends in CMOS VLSI
- Trustworthy Wireless Networks: Issues and Applications
- Nyquist Meets Bode on the z-plane
- Digital Nano-CMOS VLSI Design Courses in Electrical and Computer Engineering Through Open-Source/Free Tools
- Research: Culture and Best Practices

AIM AND SCOPE

Mobile phones and other similar applications are embedded systems designed using system-on-chip technology. These systems consist of several heterogeneous components, such as: analog/mixed-signal circuits, digital circuits, power management systems, and software. ISED brings together researchers, students, and industry personnel from these fields for the efficient design of such portable systems. ISED 2010 is sponsored by prestigious agencies of both USA and India; the National Science Foundation (NSF), USA and the Department of Science and Technology (DST), India. Several academic and industrial organizations from the USA and India generously sponsor ISED 2010.

40 STATE-OF-THE-ART PRESENTATIONS IN 10 SESSIONS

Covering the following areas of electronic system design:
- (1) Analog/Mixed-Signal System Design (AMS)
- (2) Digital System Design and Validation (DSD)
- (3) Embedded System Design (ESD)
- (4) Power Aware System Design (PSD)
- (5) CAD for Embedded System Design (CAD)
- (6) Software System and Application Design (SSD)
- (7) Emerging Technology and System Design (ETD)

WHO SHOULD ATTEND

Academic and industrial researchers in the area of electronic system design. Students in electrical science disciplines, including: computer science, computer engineering, electrical engineering, electronics engineering, and communications engineering. Junior faculty and doctoral students in electrical sciences will be in particular benefited by the Workshop for Engineering Scholars (WES).

REGISTRATION IS OPEN TO ALL

Visit the "Registration Section" of the ISED 2010 website. Reduced rates with discounts for student, IEEE members, and WES attendees are available.

GET VISIBILITY THROUGH SPONSORSHIP

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